INFORMATION DISCLOSURE CITATION	ATTY. DOCKET NO.	SERIAL NO.
	550-192	09/731,060
(Use several sheets if necessary)	APPLICANT NEVILL et al.	
	FILING DATE December 7, 2000	TC/A.U. 2122

NOV 3 0 2004

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

		DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	NO

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

✓	IBM Technical Disclosure Bulletin, August 1982, pp954-956, "On-Chip Microcoding of a Microprocessor With Most Frequently Used Instructions of Large System and Primitives Suitable for Coding Remaining Instructions".
✓	IBM Technical Disclosure Bulletin, April 1983, pp5576-5577, "Emulation Instruction".
✓	The book ARM System Architecture by S. Furber. pages 213-214.
✓	The book Computer Architecture: A Quantitative Approach by Hennessy et al pages 119-120, 259-261, 367-369, 479-480 and appendixes 46 and 47.
✓	the book The Java Virtual Machine Specification by Tim Lindholm et al., 1st and 2nd editions, page 3.
✓	H. Stone, Chapter 12-"A Pipeline Push-Down Stack Computer", 1969, pages 235-249.

*Examiner		Date Considered 5/5/05	

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INFORMATION DISCLOSURE CITATION	ATTY. DOCKET NO. 550-192		SERIAL NO. 09/731,060		
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(several sheets if necessary)	FILING DATE December 7, 2000		GROUP 2122 2127		

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dz dn	1 102 164	5/2001	EP				
dz dn	WO 99/61982	12/1999	PCT				

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

*Examiner	Date Considered	1/9/04	5/5/05

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